L Number	Hits	Search Text	DB	Time stamp
156	6	("6430478" "5525959" "6684152" "4630220"	USPAT;	2004/07/08
		"5367600" "4480298" "4884204" "4119353"	US-PGPUB;	18:30
		"4461003" "5280232" "5291962" "5388091"	EPO; JPO;	
		"5831411" "6157175" "6377352" "5616965"	DERWENT;	
		"5892893" "6144312" "4267569" "4293947"	IBM_TDB	
		"4410000" "4453113" "4784510" "4959962"	_	
		"5216674" "5247705" "5281900" "5283485"		
		"5339782" "5347109" "355573" "5357518"		
		"5444626" "5617282" "5778528" "5791933"		
		"5974351" "6152588" "6208924" "6429790"		
		"4931964" "6085134" "6650976" "4459591"	•	
		"4926352" "5025350" "5332279" "6038500"		
		"5182460" "5601142").pn. and (circuit adj board pcb)		
157	5	("6430478" "5525959" "6684152" "4630220"	USPAT;	2004/07/08
107		"5367600" "4480298" "4884204" "4119353"	US-PGPUB;	18:19
		"4461003" "5280232" "5291962" "5388091"	EPO; JPO;	10.17
•		"5831411" "6157175" "6377352" "5616965"	DERWENT;	
		"5892893" "6144312" "4267569" "4293947"	IBM_TDB	
		"4410000" "4453113" "4784510" "4959962"	1011_100	
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		"5444626" "5617282" "5778528" "5791933"		
		"5974351" "6152588" "6208924" "6429790"		
		"4931964" "6085134" "6650976" "4459591"		
		"4926352" "5025350" "5332279" "6038500"		
		"5182460" "5601142").pn. and (circuit adj board pcb)		
150	(401	and vehicle	LICDAT	2004/07/08
158	6401	(circuit adj board pcb) and (bus network	USPAT;	
		communicat\$4) and (control\$4 and regulat\$4) and	US-PGPUB;	18:32
		(\$5processor controller computer) and (storage	EPO; JPO;	
1		memory register) and (input and output i/o) and	DERWENT;	
150	22	(@ad<20000912 @rlad<20000912)	IBM_TDB	2004/07/09
159	33	700/2 and (carrier pcb circuit adj board) and (bus	USPAT;	2004/07/08
		network communicat\$4) and (control and regulat\$3)	US-PGPUB;	18:33
		and (storage memory) and (input output i/o) and	EPO; JPO;	
		(@ad<20010921 @rlad<20010921)	DERWENT;	
110	24	70010 17 1 1 1 1 1 1 1 1 1 1	IBM_TDB	2004/07/00
160	31	700/2 and (carrier pcb circuit adj board) and (bus	USPAT;	2004/07/08
		network communicat\$4) and (control and regulat\$3)	US-PGPUB;	18:34
		and (storage memory) and (input and output i/o) and	EPO; JPO;	
		(@ad<20010921 @rlad<20010921)	DERWENT;	
		700/0 1/ . 1	IBM_TDB	2004/07/00
161	23	700/2 and (carrier pcb circuit adj board) and (bus	USPAT;	2004/07/08
		network communicat\$4) and (control same regulat\$3)	US-PGPUB;	18:34
		and (storage memory) and (input same output i/o) and	EPO; JPO;	
		(@ad<20010921 @rlad<20010921)	DERWENT;	
110		700/0 1/ 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	IBM_TDB	2004/07/05
162	23	700/2 and (carrier pcb circuit adj board) and (bus	USPAT;	2004/07/08
		network communicat\$4) and (control same regulat\$3)	US-PGPUB;	18:36
		and (storage memory) and (input same output i/o) and	EPO; JPO;	
		(cpu microprocessor processor computer controller)	DERWENT;	
		and (@ad<20010921 @rlad<20010921)	IBM_TDB	

163	2	700/2 and (carrier pcb circuit adj board) and (bus	USPAT;	2004/07/08
100	_	network communicat\$4) and (plural\$3 multiple more)	US-PGPUB;	18:39
		with ((control same regulat\$3) and (storage memory)	EPO; JPO;	10.07
		and (input same output i/o) and (cpu microprocessor	DERWENT;	
		processor computer controller)) and (@ad<20010921	IBM_TDB	
		@rlad<20010921)	_	
164	1	700/2 and (carrier pcb circuit adj board) and (bus	USPAT;	2004/07/08
		network communicat\$4) and (plural\$3 multiple each)	US-PGPUB;	18:42
		with ((control same regulat\$3) and (storage memory)	EPO; JPO;	
		and (input same output i/o) and (cpu microprocessor	DERWENT;	
		processor computer controller)) and (@ad<20010921 @rlad<20010921)	IBM_TDB	
165	11	700/\$ and (carrier pcb circuit adj board) and (bus	USPAT;	2004/07/08
		network communicat\$4) and (plural\$3 multiple each)	U5-PGPUB;	18:43
		with ((control same regulat\$3) and (storage memory)	EPO; JPO;	
		and (input same output i/o) and (cpu microprocessor	DERWENT;	
		processor computer controller)) and (@ad<20010921	IBW_TDB	
		@rlad<20010921)		
166	2	700/\$ and (carrier pcb circuit adj board) and (bus	USPAT;	2004/07/08
		network communicat\$4) and (plural\$3 multiple each)	US-PGPUB;	18:44
		with ((control same regulat\$3) same (storage	EPO; JPO;	
		memory) same (input same output i/o) same (cpu	DERWENT;	
		microprocessor processor computer controller)) and	IBW_TDB	
		(@ad<20010921 @rlad<20010921)		
167	1	700/\$ and (bus network communicat\$4) and (carrier	USPAT;	2004/07/08
		pcb circuit adj board) same (plural\$3 multiple each)	US-PGPUB;	18:45
		with ((control same regulat\$3) same (storage	EPO; JPO;	
		memory) same (input same output i/o) same (cpu	DERWENT;	
÷		microprocessor processor computer controller)) and (@ad<20010921 @rlad<20010921)	IBM_TDB	
168	1	700/\$ and (bus network communicat\$4) and (carrier	USPAT;	2004/07/08
		pcb circuit adj board) same (plural\$3 multiple each)	US-PGPUB;	18:46
		with ((control regulat\$3) same (storage memory)	EPO; JPO;	
		same (input same output i/o) same (cpu	DERWENT;	
		microprocessor processor computer controller)) and	IBM_TDB	
		(@ad<20010921 @rlad<20010921)		
169	2	700/\$ and (bus network communicat\$4) and (carrier	USPAT;	2004/07/08
		pcb circuit adj board) same (control regulat\$3) same	US-PGPUB;	18:47
		(plural\$3 multiple each) with ((storage memory) same	EPO; JPO;	
		(input same output i/o) same (cpu microprocessor	DERWENT;	
		processor microcomputer computer controller)) and	IBW_TDB	
		(@ad<20010921 @rlad<20010921)		
170	4	700/\$ and (bus network communicat\$4) and (carrier	USPAT;	2004/07/08
		pcb circuit adj board) same (control regulat\$3) same	US-PGPUB;	18:50
		(plural\$3 multiple each) with ((storage memory) same	EPO; JPO;	
		(cpu microprocessor processor microcomputer	DERWENT;	
		computer controller)) same (input same output i/o) and (@ad<20010921 @rlad<20010921)	IBM_TDB	
171	91	(bus network communicat\$4) and (carrier pcb circuit	USPAT;	2004/07/08
~! *	'1	adj board) same (control regulat\$3) same (plural\$3	US-PGPUB;	18:51
		multiple each) with ((storage memory) same (cpu	EPO; JPO;	
		microprocessor processor microcomputer computer	DERWENT;	
		controller)) same (input same output i/o) and	IBM_TDB	
		(@ad<20010921 @rlad<20010921)		

172	59	(bus network communicat\$4) same (carrier pcb	USPAT;	2004/07/08
		circuit adj board) same (control regulat\$3) same	US-PGPUB;	18:52
		(plural\$3 multiple each) with ((storage memory) same	EPO; JPO;	
		(cpu microprocessor processor microcomputer	DERWENT;	
		computer controller)) same (input same output i/o)	IBM_TDB	
		and (@ad<20010921 @rlad<20010921)		
173	0	distribut\$4 and vehicle and (bus network	USPAT;	2004/07/08
		communicat\$4) same (carrier pcb circuit adj board)	US-PGPUB;	18:52
		same (control regulat\$3) same (plural\$3 multiple	EPO; JPO;	
		each) with ((storage memory) same (cpu	DERWENT;	
		microprocessor processor microcomputer computer	IBM_TDB	
		controller)) same (input same output i/o) and		
		(@ad<20010921 @rlad<20010921)		
174	35	(distribut\$4 vehicle) and (bus network	USPAT;	2004/07/08
		communicat\$4) same (carrier pcb circuit adj board)	U5-PGPUB;	18:54
		same (control regulat\$3) same (plural\$3 multiple	EPO; JPO;	
		each) with ((storage memory) same (cpu	DERWENT;	
		microprocessor processor microcomputer computer	IBM_TDB	
		controller)) same (input same output i/o) and		
		(@ad<20010921 @rlad<20010921)		
175	35	(distribut\$4 vehicle) and (bus network	USPAT;	2004/07/08
		communicat\$4) same (carrier pcb circuit adj board)	US-PGPUB;	18:54
		same (control regulat\$3) same (input same output	EPO; JPO;	
		i/o) same (plural\$3 multiple each) with ((storage	DERWENT;	
		memory) same (cpu microprocessor processor	IBW_TDB	
		microcomputer computer controller)) and		
		(@ad<20010921 @rlad<20010921)		
177	76	(distribut\$4 and vehicle) and (bus network	USPAT;	2004/07/08
		communicat\$4) same (carrier pcb circuit adj board)	US-PGPUB;	18:55
		same (control regulat\$3) same (input same output	EPO; JPO;	
		i/o) same (storage memory) same (cpu	DERWENT;	
		microprocessor processor microcomputer computer	IBW_TDB	
		controller) and (@ad<20010921 @rlad<20010921)		
178	0	(distribut\$4 and vehicle) same (bus network	USPAT;	2004/07/08
		communicat\$4) same (carrier pcb circuit adj board)	US-PGPUB;	18:55
		same (control regulat\$3) same (input same output	EPO; JPO;	
		i/o) same (storage memory) same (cpu	DERWENT;	
		microprocessor processor microcomputer computer	IBW_TDB	
		controller) and (@ad<20010921 @rlad<20010921)		
179	95	(distribut\$4 vehicle) same (bus network	USPAT;	2004/07/08
		communicat\$4) same (carrier pcb circuit adj board)	US-PGPUB;	18:55
		same (control regulat\$3) same (input same output	EPO; JPO;	
		i/o) same (storage memory) same (cpu	DERWENT;	
		microprocessor processor microcomputer computer	IBW_TDB	
100		controller) and (@ad<20010921 @rlad<20010921)		
180	52	700/\$ and (bus network communicat\$4) same	USPAT;	2004/07/08
		(carrier pcb circuit adj board) same (control	US-PGPUB;	18:56
		regulat\$3) same (input same output i/o) same	EPO; JPO;	
		(storage memory) same (cpu microprocessor	DERWENT;	
Ì		processor microcomputer computer controller) and	IBW_TDB	
		(@ad<20010921 @rlad<20010921)	<u> </u>	

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5280232 5291962 5525959 5831411 6377352 4884204 4119353 4461003 5388091 6157175 6430478 5924755 6114970 4903435 5247808 5491383 5521588 5696777 5892893 5952917 6006870 6113138 6144312 6163985 6226497 4018052 4114717 4124096 4271388 4276832 4293947 4354173 4425564 4494765 4494765 4506258	88888777777777777777777777777777777777
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